

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hideaki Kuwabara et al. Art Unit : 2822
Serial No. : 10/735,767 Examiner : Kevin Picardat
Filed : December 16, 2003 Conf. No. : 4086
Title : SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME

MAIL STOP AF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

This statement is being filed after a final Office action or a Notice of Allowance, but before payment of the issue fee. Each item of information in this statement (*i*) was cited in a communication from a foreign patent office in a counterpart foreign application, the communication relating to counterpart Japanese Patent Application No. 2002-377816 and being dated September 18, 2007, which is not more than three months prior to the filing of this statement, and (*ii*) was not first cited in any communication from a foreign patent office in a counterpart application which was more than three months prior to the filing date of this statement. References "AA" through "AJ" are English-language documents corresponding to reference "AL" and are cited in accordance with MPEP 609(III)(B)(5).

The fee in the amount of \$180 in payment of the late submission fee of §1.17(p) is being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: September 27, 2007

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